

Application No. Not Yet Assigned
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : Not Yet Assigned

Applicants : Hideo SENOO et al.

Filed : Concurrently Herewith

Title : PROTECTIVE STRUCTURE OF
SEMICONDUCTOR WAFER, METHOD
FOR PROTECTING SEMICONDUCTOR
: WAFER, MULTILAYER PROTECTIVE SHEET
USED THEREIN, AND METHOD FOR
: PROCESSING SEMICONDUCTOR WAFER

International Application No. : PCT/JP2003/010570

International Filing Date : 21 August 2003

Priority Dates Claimed : 28 August 2002
4 December 2002

MAIL STOP PCT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to initial examination, please amend the above-identified patent application as follows:

Amendments to the Specification begin on page 1 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Amendments to the Abstract begin on page 7 of this paper.

Remarks begin on page 8 of this paper.